DINESH SINGH

H. no. 82, Shakti Nagar,
Near Mansarovar School, Naveen Nagar, Moradabad
Contact No.: +91-8377920113
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KEY SKILLS:

- LANGUAGES: **PYTHON, C, OOPS CONCEPT**
- HARDWARES/Microcontroller: **Raspberry pi**, **Ultrasonic Sensor, GSM (Sim900), PIC**
- SIMULATION TOOLS: **LAB VIEW**, **Cadence Virtuoso, Proteus Professional, Xilix ISE**
- IOT CONCEPTS: **TCP/IP**
- OS: **Windows**

EXPERIENCE:

- **Designation: Asst. Professor at RVIT Bijnor**
  Duration : September 2021 – till now

- **Designation: Asst. Professor at KITPS Moradabad**
  Duration : November 2020- June 2021

- **Designation : Asst. Professor at VIT Aligarh**
  Duration: October 2018 –April 2019

TRAINING AND CERTIFICATIONS:

- **5 day HUMAN VALUE** certificate from 7Feb2022-11Feb 2022 organized by **AICTE**.
- Attended two session of faculty development programme (FDP) in M.Tech – (a) **Wireless And Mobile Communication** (b) **Satellite Communication.**
- **GATE Exam qualified** with score of 311 in Gate 2015.
- Attended a training program of 45 days on Embedded System at Vaskoe Technologies Private Limited, Lucknow

EDUCATION:

- **2018: M.Tech. in VLSI Design**
  Ajay Kumar Garg Engineering College, Ghaziabad
  Percentage: 67.00%
- **2013: B.Tech. in Electronics and Communication Engineering**
  Moradabad Institute of Technology, Moradabad.
  Percentage: 62.50%
- **2009: Intermediate**
  Kendriya Vidyalaya Moradabad
  Percentage: 62.40%
- **2007: High School**
  R.S.D Academy, Moradabad
  Percentage: 66.17%

M.TECH THESIS:

- **TITLE: Electronically Tunable Signal Processing Circuit**:It is an analog universal filter circuit based on Operational Transconductance Amplifier that works on different frequencies. It consists only two OTAs that provide less consumption of area, cost effective with low power dissipation.
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PUBLICATIONS:

- **Journal – 01**
  Electronically Tunable Signal Processing Circuit in International Journal of Advance Research in Science and Engineering on 10 October 2017.
- **National conference – 01**
  Electronically- Tunable Current-Mode Biquad Design Using MO-OTAs in Emerging Trends in Electronics and Communication, (ETEC-2017) on July 2017.

SEMINAR:

- Delivered a session on **'Mobile Security Threats and Dutems'** during B.Tech. 6th semester
- Delivered a session on **'Scope of Gallium Nitride in Semiconductor'** during M.Tech. 3rd semester.
- Delivered a session in **National Conference at Ajay Kumar Garg Engineering College Ghaziabad** on **'Electronically- Tunable Current-Mode Biquad Design Using MO-OTAs'** during M.Tech. 3rd semester.

PROJECTS:

- **Project 1: IOT based home appliance control system**
- **Project 2:** Wireless Control Robot
  Skills Used: Embedded C, PIC (microcontroller)
- **Project 3**: CRO
- Calculator using **LAB VIEW**
- **Luggage security system** using 555 IC
- PNP & NPN transistor testor
- Two-minute Timer Circuit with 230 V A.C Supply

PERSONAL DETAILS:

Date of Birth	:	25 December, 1989
Father's Name	:	Late Shri. Ram Bharosay Lal
Mother's Name	:	Late Smt. Phoolwati Devi
Nationality	:	Indian
Sex	:	Male
Marital Status	:	Married
Languages Known	:	English, Hindi
Correspondence Address	:	H.no-82, Shakti Nagar near Mansarovar School Naveen Nagar, M.D.A Collony Moradabad (Pin-244001).
Permanent Address	:	H.no-15, Village-Bendukhera, Post-Said Nagar, Tehseel Tanda District Rampur (Pin-244925)

DECLARATION:

I hereby declare that the above given information are true and to the best of my knowledg

(DINESH SINGH)