Curriculum Vitae

NITISH NARAYAN

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Colony Jansath Muzaffarnagar [U.P.]
Pin-251314

Career Objective

Intend to build a career with leading corporate of hi-tech environment with committed and dedicated peoples, which will help me to explore myself fully and realize my potential. Willing to work as key player in challenging and creative environment.

Area of Interest

- Electronics Core
- Embedded Systems(Microcontroller & Microprocessor)

Educational Qualification

Course (Stream)/Examination	Institution/University	Month/ Year of Passing	Performance	(%) Percentage
Highschool	D.A.V. Inter Collage / U.P. Board Allahabad	June/2004	Pass (1st)	63.4%
Intermediate	G.C. Public School/C.B.S.E. Board New Delhi	June/2007	Pass(1st)	70.04%
Graduation B.tech (E.C.E)	B.I.T. Muzaffarnagar/G.B.T.U. Lucknow	June/2011	Pass(1st)	70.00%
Post Graduation M.Tech (E.C.E)	B.I.T. Muzaffarnagar/M.T.U. Noida	June/2014	Pass(1st)	72.00%

Key Skills

- Hard working, Can work in any environment
- Honest, Dedicated

Technical Skills:

- Basics of computer.
- Six months diploma in MIS(Management Information Systems) from APTECH Pvt. Ltd.
- Worked on Embedded software TOP VIEW SIMULATOR.
- Worked on Ansoft HFSS (High Frequency Structure Simulator).

Trainings

- Six weeks project training at CETPA Roorkee on EMBEDDED SYSTEMS in the year 2010.
- Vocational training of 1- month from BHEL Haridwar in the year 2010.
- 6-month Practical training in INSTRUMENTATION ENGINEERING at BILT YamunaNagar in 2011.

Experience:

- Worked as an RF Engineer in R&D Department at Bhagwati Products Limited ,Rudrapur.

Achievements

- Attended a Workshop on Emerging trends in MICROWAVE Integrated Circuits sponsored by **IEEE** & **MTT-S** conducted by Deptt. Of Electronics and Communication Engineering at Bhagwant Institute of Technology on 13th March 2010.
- Presented a seminar on HDSS (Holographic Data Storage Systems) as partial fulfillment of degree.
- Presented a paper in **IEEE** National Conference on **TRENDS IN SIGNAL PROCESSING & COMMUNICATION (TSPC)** on the topic WIDEBAND RECONFIGURABLE ANTENNA FOR MULTIBAND APPLICATION held on 12-14 April 2014 at Bhagwant Institute of Technology Muzaffarnagar.
- Dissertion of M.tech has been completed on "DESIGN AND ANALYSIS OF RECONFIGURABLE MULTIBAND INTEGRATED ANTENNAS WITH RF-MEMS SWITCHES" for the fulfillment of P.G. Degree in June 2014.

Academic Projects:

- Three Motor Robotic Arm (Training Project)
- Password Locking System(Training Project)
- Vehicle Tracking System (Microcontroller Based)

Personal Vitae

Father's Name : Shri Sukhvir Deo

Mother's Name :Smt. Saroj Bala

Date of Birth : 11th of July 1989

Gender : Male

Marital Status : Single

Nationality : Indian

Language Known : English , Hindi

Declaration:

I hereby declare that all the information mentioned above by me is correct to the best of my knowledge.

Place: Jansath

NITISH NARAYAN