VINEET SHARMA



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OBJECTIVE

To utilize my technical skills and provide a professional service to society by applying and utilizing my knowledge and working in a challenging and motivating working environment. Hence, contributing towards the development of the Nation.

EXPERIENCE

Asians Institute of Technology, (R.T.U), Tonk, Jaipur 16/09/2014 - 16/07/2019
Assistant Professor in the department of Electrical engineering.
Departmental Coordinator.
Teaching.
Thesis supervisor.
Counseling.

Dr. K N Modi University, Newai, Rajasthan 13/02/2013 - 15/09/2014
Assistant Professor in the department of Electrical engineering
Teaching.
Academics.
Research Assistant.

EDUCATION

Motilal Nehru National Institute of Technology, Allahabad, Prayagraj 2019-2023*
PhD (Pursuing) in Electrical Engineering (Control Systems).
7.0

J. C Bose University of Science and Technology (YMCA), Faridabad (Govt.) 2013
M.Tech in Control and Instrumentation.
7.465

Dehradun Institute of Technology, Dehradun 2009
B.Tech in Electrical Engineering.
68.72

ACHIEVEMENTS & AWARDS

Selected by AICTE, India for pursuing Ph.D. in Quality Improvement Program (QIP) category at Motilal Nehru National Institute of Technology, Allahabad, Prayagraj.

SKILLS

Diploma in Computer Application. | MATLAB Software. | SCADA | MS-Word. | P-Spice Software | MS-Excel | LaTex

PROJECTS

Singular Value Decomposition-based Model Order Reduction
Model Order reduction is a technique by which order of a system is reduced so that mathematical competencies may get vanished by retaining the basic and important characteristics of the system.

Performance analysis of Boost Converter using PI controller.
In control systems PI control means Proportional Integral controller which is used to minimise the error and create the system very effective and efficient. With the help of PI controller boost converter is analysed and modified accordingly.

Speed Control of DC Motor using ANN Technique
Artifical Neural Network is a technique which behaves like artificial brain and solve the problem formulated by training and then minimizing the error. This technique is implemented in DC motor and its speed is caliberated.

PUBLICATIONS

Confined Frequency-interval Gramian framework-based balanced model reduction (under review in IETE Journal).
This research is based on concept of balanced truncation of frequency interval using transformation matrices and controllability and observalbility Gramians for obtained the reduced order model of a system.

SVD based Model Reduction of Frequency Weighted Realization (IEEE conference in PEDES) to be held in December 2022
This is based on model order reduction of frequency weighted based balanced realization of a system.

INTERESTS

Participating in social awareness programs regularly like Blood donation.

Playing badminton.

Playing Chess.

ACTIVITIES

IEEE Student member.

ISTE Lifetime member.

Attended 1 ATAL one-week FDP program organized at Motilal Nehru National Institute of Technology, Allahabad, Prayagraj,India.

Attended 5 one-week courses organized by NITTTR Chandigarh, sponsored by AICTE, India.

Participated in one week STC organized by Motilal Nehru National Institute of Technology, Allahabad, Prayagraj, India.

Participated in one week STC organized by National Institute of Technology, Jalandhar, India.

Organising member of 2 National conferences in 2015 & 2016 on National Conference on Energy and Environment & National Conference on Energy Environment and Pollution respectively.

Organising member of National Science Congress 2014 held in Dr. KN Modi University, Newai.

Participated and presented papers in 4 International conferences.

Participated and presented papers in 8 National Conferences.

LANGUAGES

Hindi English

PERSONAL DETAILS

Date of Birth : 08/02/1988

Marital Status : Single

Nationality : Indian

REFERENCE

Dr. Deepak Kumar - "Motilal Nehru National Institute of Technology, Allahabad"
Assistant Professor-Electrical Engineering
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Dr. H.P Tiwari - "Malviya National Institute of Technology, Jaipur"
Professor - Electrical Engineering
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Dr Munish Vasistha - "JC Bose University of Science and Technology, Faridabad"
Professor - Electronics and Electrical Engineering.
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